



50 MHz Arbitrary Waveform Generator The LXI interface makes easier for the test system !

Function Generator



The FGA5050 is a function generator that equips with the arbitrary waveform function. In addition to Sine waveform, Square waveform, Ramp waveform of those custom waveform generation function, the FGA5050 offers to realize high precision waveform with 1 μ Hz of resolution and 50MHz of wideband frequency. The FGA5050 can be used in wide application such as "Voltage variation test for Automotive Electronic Components", "ECU false signal source", "Charge-Discharge test for the rechargeable battery", "Ripple super-impose test" and it can be used as the trigger signal for the various type of test system.

Further more, three types of interface, LAN / USB / GPIB* are equipped with the FGA5050 as standard feature, it applies for automated test along with manual operation.

Wide band frequency

- Sine waveform : 1μ Hz to 50MHz, Square waveform : 1μ Hz to 25MHz
- Sine waveform, Square waveform, Ramp waveform, Triangle waveform, Pulse waveform, Noise waveform, DC, Arbitrary waveform output
- Waveform Editor Application Software "WAVEPATT" is included as standard
- Various modulation types
- AM, FM, PM, FSK, PWM, Frequency sweep, Burst, External Modulation Input
- 16 bits / up to 50MHz pattern out
- 14 bits / 256k-point, 125MSs/s
- 10MHz clock in and out
- Trigger Input and Trigger output (TTL compatible)
- Interface : LAN / USB / GPIB*standard

*Only available in Model FGA5050GC

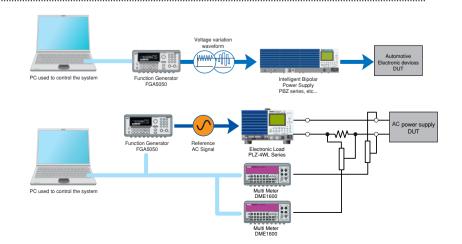
Application

Voltage variation test for Automotive Electronic devices

The system combined with the FGA5050 and the Bipolar power supply, it can be used as the "Signal Source" for the "Voltage variation test of the automotive electronic components" complied to the ISO standard and other manufacturer's standard.

Measurement of the output impedance of the power supply

The system combined with the FGA5050, electronic load, and multi-meter, it can be used as the "Reference AC Signal" for the "Impedance measurement of power supply output".



Specifications

Waveform Chara	Standard waveforms	Sine, Square, Ramp. Tr	iangle, Pulse, No	pise, DC	
Waveform	Built-in arbitrary waveforms	Sine, Square, Ramp, Triangle, Pulse, Noise, DC ns Exponential Rise and Fall, Negative ramp, Sin(x)/x, cardiac			
		1 µHz to 50 MHz	ant Hoganio iai		
	Frequency	< 100 kHz			
	Amplitude				
	Flatness *1 *2	< 5 MHz	0.15 dB		
	(Relative to 1 kHz)	< 20 MHz	0.3 dB		
		< 50 MHz	0.5 dB		
		DC to 20 kHz	< 1 Vpp	-70 dBc	
			≥ 1 Vpp	-70 dBc	
		20 kHz to 100 kHz	< 1 Vpp	-65 dBc	
			≥ 1 Vpp	-60 dBc	
Sine	Harmonic	100 kHz to 1 MHz	< 1 Vpp	-50 dBc	
	distortion *2 *3		≥ 1 Vpp	-45 dBc	
		1 MHz to 20 MHz	< 1 Vpp	-40 dBc	
			≥ 1 Vpp	-35 dBc	
		20 MHz to 50 MHz	< 1 Vpp	-35 dBc	
		20 10112 10 30 10112	≥ 1 Vpp	-30 dBc	
	Total Harmonic distortion	DC to 20 kHz	< 0.5 Vpp	≤ 0.06 %	
	Spurious *2 *4	DC to 1 MHz		-70 dBc	
	(non-harmonic)	1 MHz to 50 MHz		-70 dBc+6 db/ octave	
	Phase Noise (10 kHz Offset)	≥1 MHz	≥ 0.1 Vpp	-115 dBc/Hz typical	
	Frequency	1 µHz to 25 MHz	1	I	
	Rise / Fall time	< 10 ns			
	Overshoot	< 2 %			
		< 10 MHz	20 % ~ 80 %		
Square	Variable Duty Cycle	< 25 MHz	40 % ~ 60 %		
	Asymmetry	< 25 MHZ 1% of period +5 ns (@			
	Asymmetry		oo /o uuty/		
	Jitter (RMS)	≥0.1 Vpp ≥1 MHz	200 ps		
Ramp,	Frequency	1 µ Hz to 200 kHz			
Triangle	<u> </u>	Linearity < 0.1 % of peak output			
mangie	Symmetry	0.0 % to 100.0 %			
	Frequency	500 µ Hz to 10 MHz			
	Pulse width	20 ns minimum			
	T dise width	10 ns res. (period \leq 10 s)			
Pulse	Variable Edge Time	< 10 ns to 100 ns			
	Overshoot	< 2 %			
	Jitter (RMS)	≥ 0.1 Vpp	200 ps		
	JILLEI (HIWIJ)	≥ 50 kHz	200 ps		
Noise	Bandwidth	20 MHz typical			
	Frequency	1 µ Hz to 10 MHz			
	Length	2 K to 256 K			
	Resolution	14 bits (including sign)			
	Sample Rate	125 M Sa/s			
Arbitrary	Min Rise / Fall time	30 ns typical			
	Linearity	< 0.1 % of peak output			
	Setting Time	< 250 ns to 0.5 % of finai value			
	Jitter (RMS)	6 ns+300 ppm			
	Non-voltage Memory	4 Waveforms * 256 K points			
Common Charac					
Frequency	Resolution	1 μ Hz			
1		1 μ HZ 10 mVpp to 10 Vpp in 50 Ω			
	Range	20 mVpp to 20 Vpp in No Load (open-circuited)			
Amplitude	Accuracy *2 *5 (at 1 kHz)	± 1 % of setting ± 1 mVpp			
	Units				
	Resolution	Vpp、Vrms、dBm 4 digits			
DC Offset		-			
	Range	± 5 V in 50 Ω ± 10 V in No Load (open-circuited)			
	Accuracy *2 *5(at 1 kHz)	± 2% of offset setting =		tude setting	
	Resolution	-	- 0.0 % UI AIIIPII	luuo selliny	
	Impedance	4 digits			
Main Output	Isolation	50 Ω typical 42 Voesk maximum to earth			
	Protection	42 Vpeak maximum to earth Short-circuit protection, Stop the output automatically at the state of over-loa			
latera d	FIGRECION		Stop the output a	automatically at the state of over-loa	
nternal Frequency	Accuracy *5	± 10 ppm in 90 days ± 20 ppm in 1 years			
External Frequency Input	Lock Range	10 MHz \pm 500 Hz			
	Level	100 mVpp ~ 5 Vpp			
	Impedance	1 kΩ typical, AC coupled			
	Lock Time	< 2 sec			
	Lock Range	< 2 sec 10 MHz			
		632 mVpp (0 dBm) typical			
External	Level	632 m\/nn (0 dRm) +//			
External Frequency	Level				
External	Impedance	50 Ω typical, AC couple			
External Frequency					

	Pavi, 1 ivi, 1	W, FSK, PWW	I, SWEEP and BURST		
	Carrier		Sine, Square, Ramp, Arb		
	Source		Internal / External		
AM	Internal M		Sine, Square, Ramp, Triangle, Noise, Arb		
	Frequency	(Internal)	2 mHz to 20 kHz		
	Depth Carrier		0.0 % to 120.0 % Sine, Square, Ramp, Arb		
	Source		Internal / External		
FM	Internal M	odulation	Sine, Square, Ramp, Triangle, Noise, Arb		
	Frequency (Internal)		2 mHz to 20 kHz		
	Deviation	()	DC to 25 MHz		
	Source		Internal / External		
	Internal Modulation		Sine, Square, Ramp, Triangle, Noise, Arb		
PM	Frequency (Internal)		2 mHz to 20 kHz		
	Deviation		0.0 ° to 360 °		
	Carrier		Pulse		
	Source		Internal / External		
PWM	Internal M		Sine, Square, Ramp, Triangle, Noise, Arb		
	Frequency (Internal)		2 mHz to 20 kHz		
	Deviation		0 % to 100 % of pulse width		
	Carrier Source		Sine, Square, Ramp, Arb Internal / External		
FSK	Internal Modulation				
	Frequency		50 % duty cycle Square 2 mHz to 100 kHz		
External	Voltage Ra		± 5 V full scale		
Modulation	Input Resi	-	8.7 kΩ typical		
Input *6	Bandwidth		DC to 20 kHz		
	Waveforms		Sine, Square, Ramp, Arb		
	Туре		Linear, Log		
SWEEP	Direction		Up or Down		
SWEEP	Sweep Time		1 ms to 500 s		
	Trigger So	urce	Internal, External or Manual		
	Marker		falling edge of sync signal (programmable frequency)		
	Waveforms*7		Sine, Square, Ramp, Triangle, Noise, Arb		
	Туре		Internal / External		
BURST	Start / Stop Phase		-360 * to +360 *		
	Internal Period Gated Source		$1 \mu s$ to 500 s		
			External trigger		
	Trigger Source		Internal, External or Manual		
	Level Slope		TTL compatible Rising or Falling (Selectable)		
Trigger	Pulse widt	h	≥ 100 ns		
Input	Impedance		≥ 10 kΩ DC coupled		
	Latency		< 500 ns		
	Level		TTL compatible into $\geq 1 \text{ k}\Omega$		
	Pulse widt	h	≥ 400 ns		
Trigger	Impedance	Э	50 Ω typical		
Output	Maximum	rate	1 MHz		
	Fan-out		≤ 4 FGA5050s		
Pattern Mode Ch	aracteristic				
	Clock Max	imum Rate	50 MHz		
Output	Output Lev	vel	TTL compatible into $\ge 2 k \Omega$		
- sipor	Output Imp		110Ω typical		
	Pattern Le	ngth	2 K to 256 K		
General		(00)			
			40 Vac (single phase) / 50 Hz/60 Hz		
Power consumption 80 VAmax					
Operating Temperature range		0 °C to 55 °C			
Operating Humidity range Storage Temperature range		30 %rh(0 °C, 50 °C), 40 %rh(18 °C, 23 °C, 28 °C), 80 %rh(35 °C), non condensing -40 °C to 70 °C			
		-40 C to 70 C			
Dimensions / Weight		224 W × 107 H × 380 D mm / 4.08 kg			
Interfaces		LAN, USB, GPIB (only GC)			
Accessories		"Power cable" 1pc. (with 3P plug), "Pattern generator cable" 1pc., "USB cable" 1pc., "CD-R"1pc., Packing list, "For Safety documents" 2pcs. (1 each for English, Japanese)			
Electromagnetic compatibility (EMC)		Conforms to the requirements of the following directive and standard. EMC Directive2004/108/EC EMC: EN61326-1:2006 EMI: CISPR 11:2003 Class A, IEC61000-3-2:2000 IEC61000-3-3:1994+A1:2001 EMSIEC61000-4-2:1995+A1:1998+A2:2000, IEC61000-4-3:2002 IEC61000-4-1:993+A1:1000, IEC61000-4-6:1996+A1:2000 IEC61000-4-1:993+A1:2000, IEC61000-1:1:2004			

*including the "Operation Manual" and "Communication Interface Manual"

Add 11/0th of output amplitude and offset spec per °C for operation outside the range of 18 2 Autorange enabled
DC offset set to 0V
Specific and the set of the specific and the set of the specific and specific and square waveforms above 10 MHz are allowed only with an "infinite" burst count

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